

FIG. 1

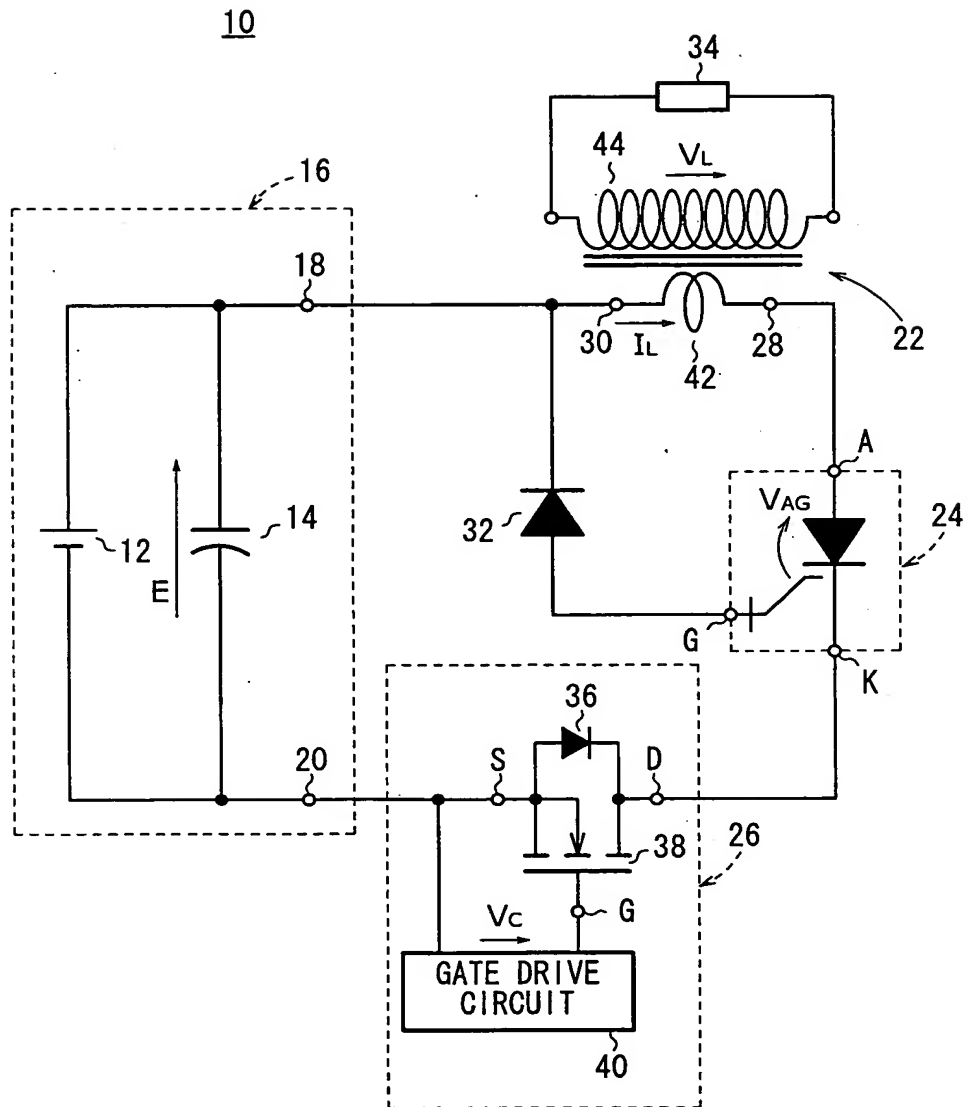


FIG. 2A

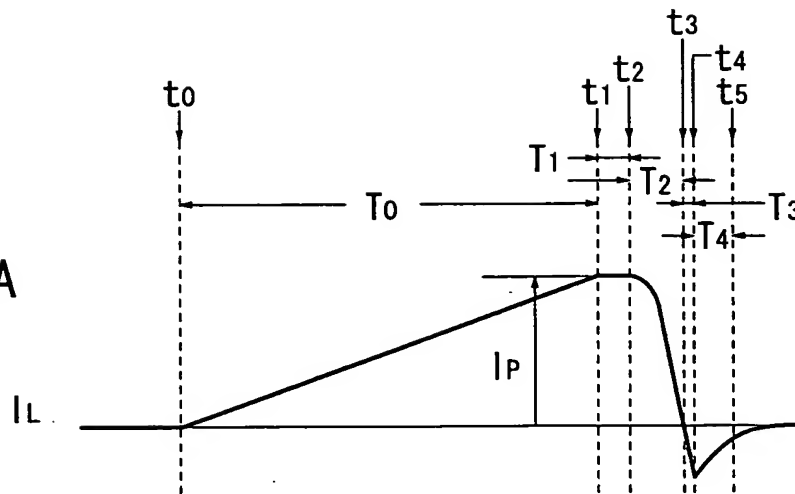


FIG. 2B

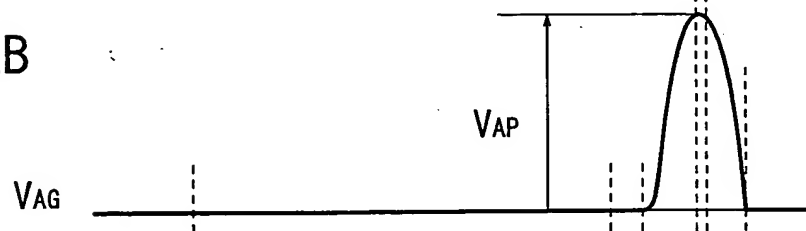


FIG. 2C

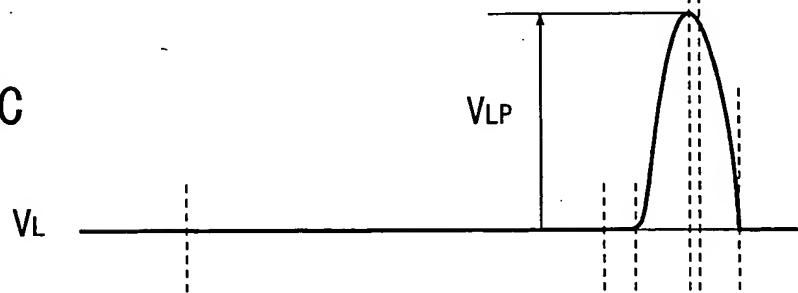


FIG. 2D
CONDUCTION STATE
OF 1ST
SEMICONDUCTOR
SWITCH 24

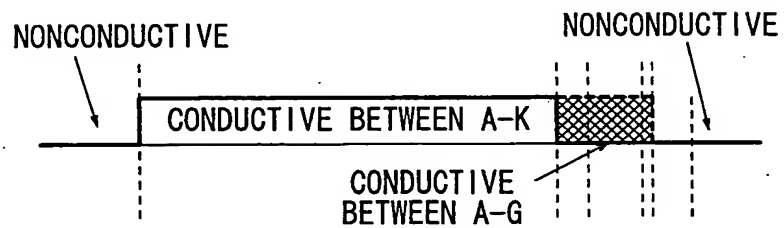


FIG. 2E
CONTROL SIGNAL V_c
FOR 2ND
SEMICONDUCTOR
SWITCH 26

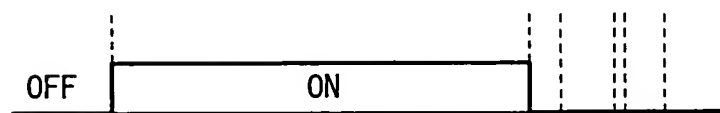


FIG. 3

10A

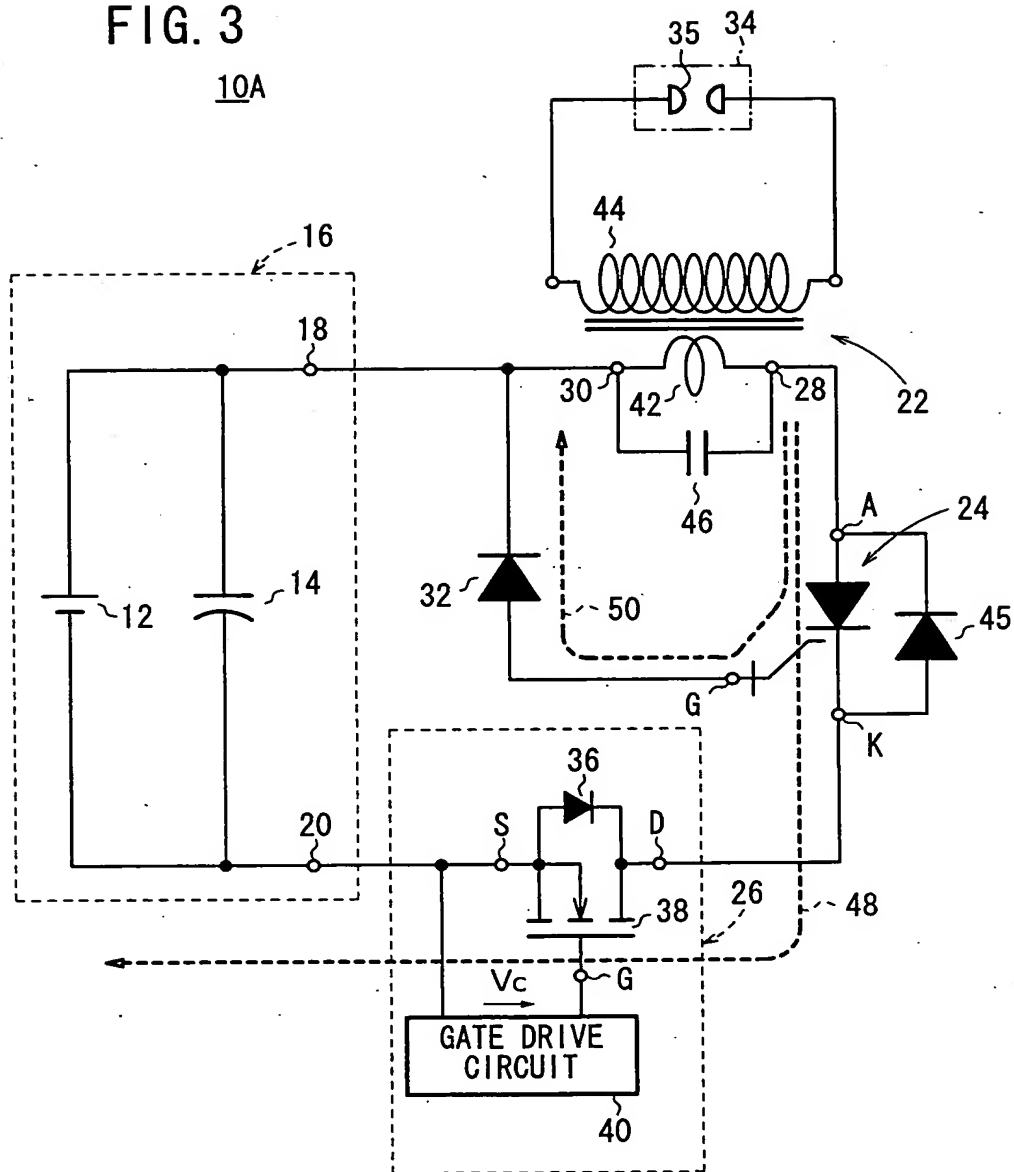
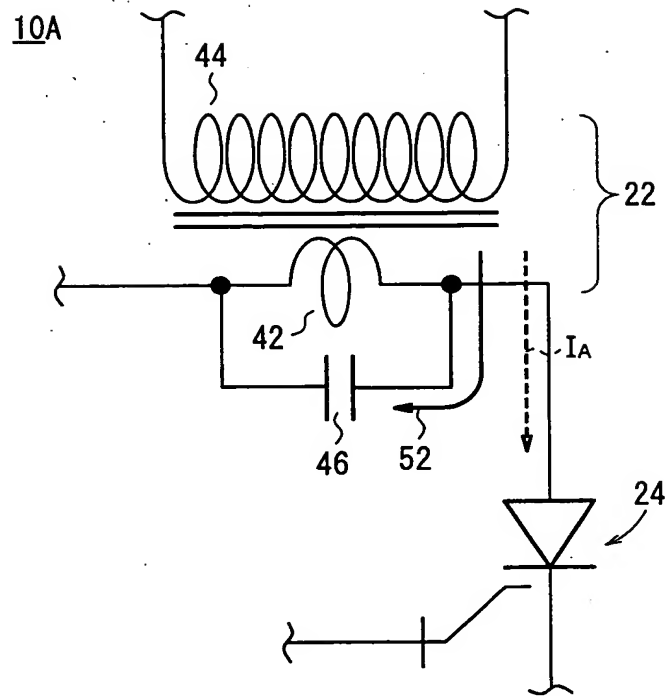


FIG. 4



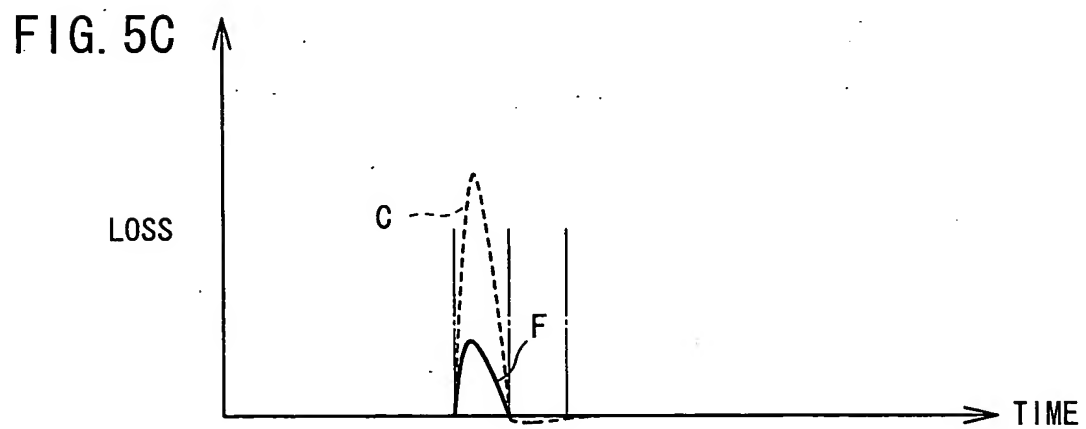
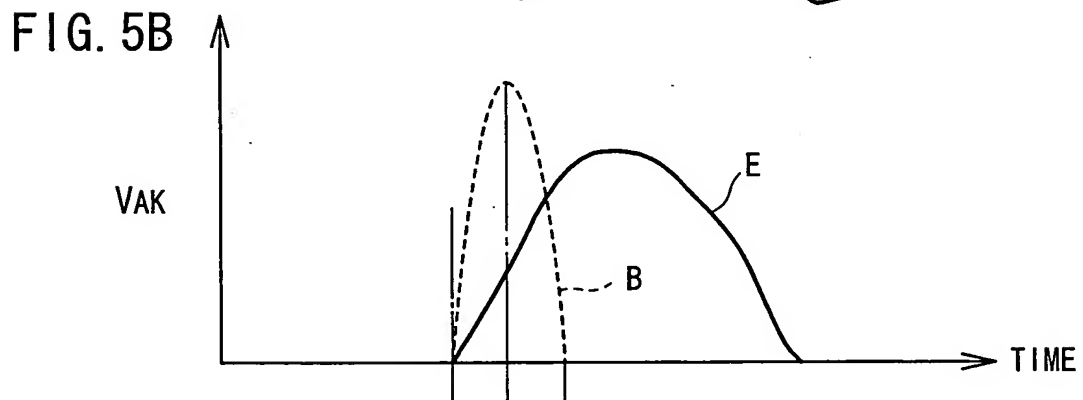
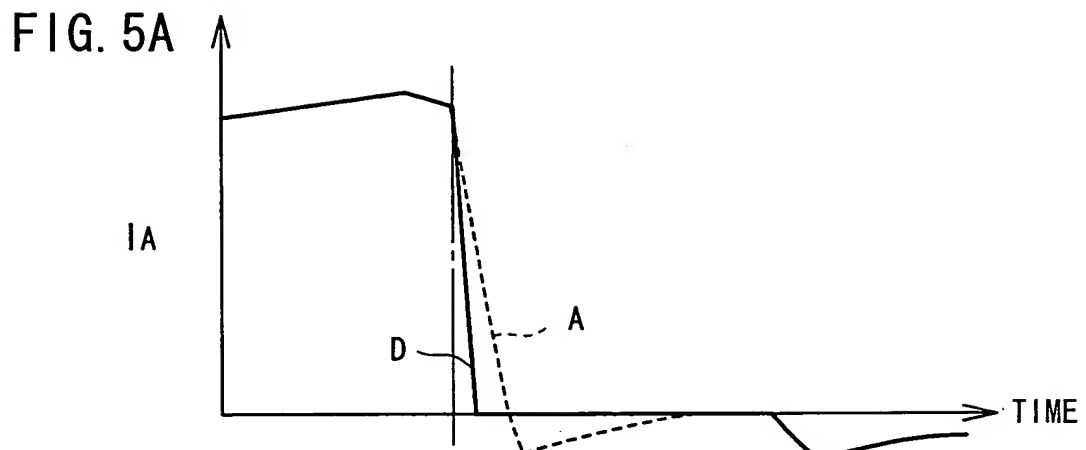


FIG. 6

10A

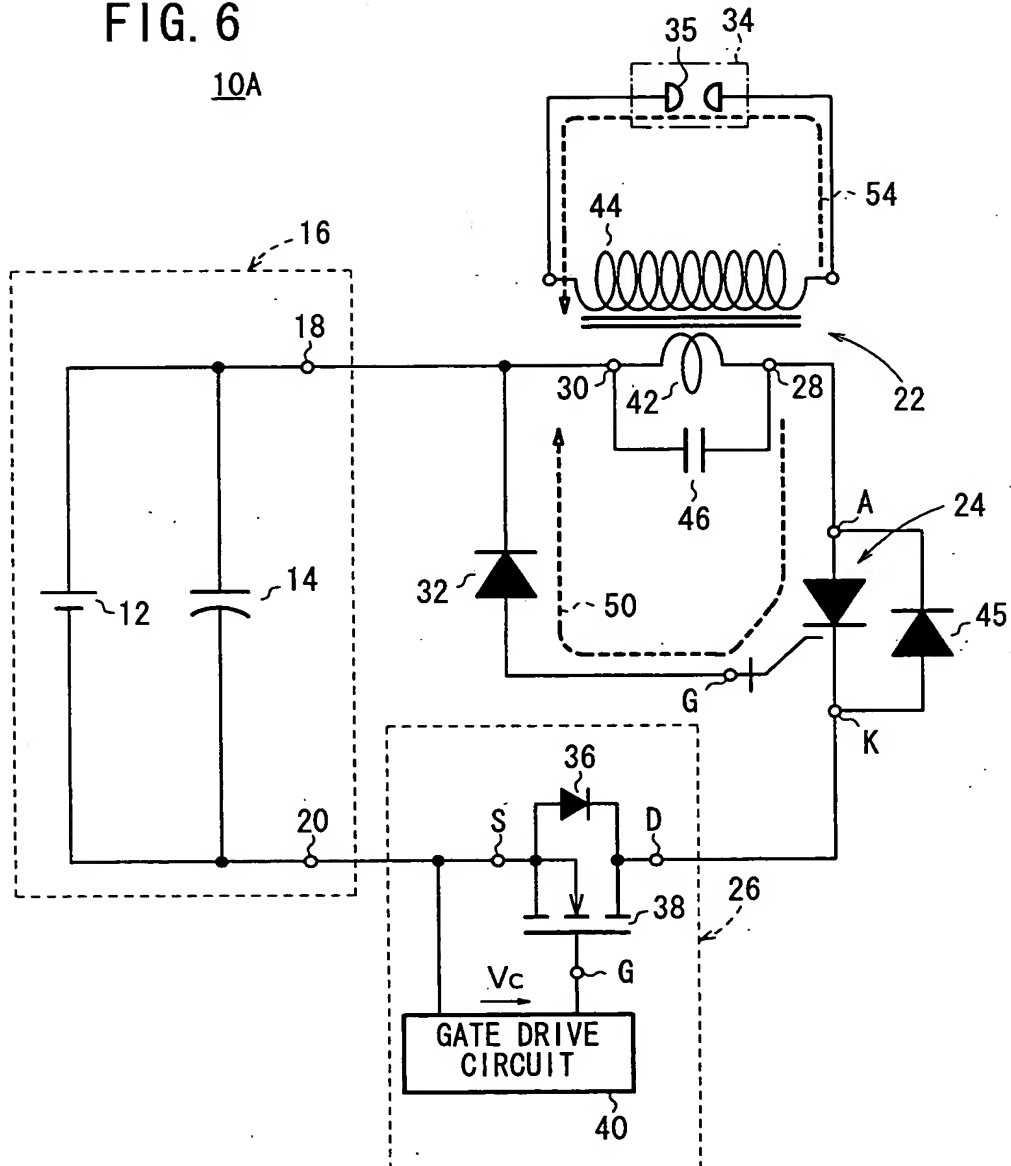


FIG. 7

10A

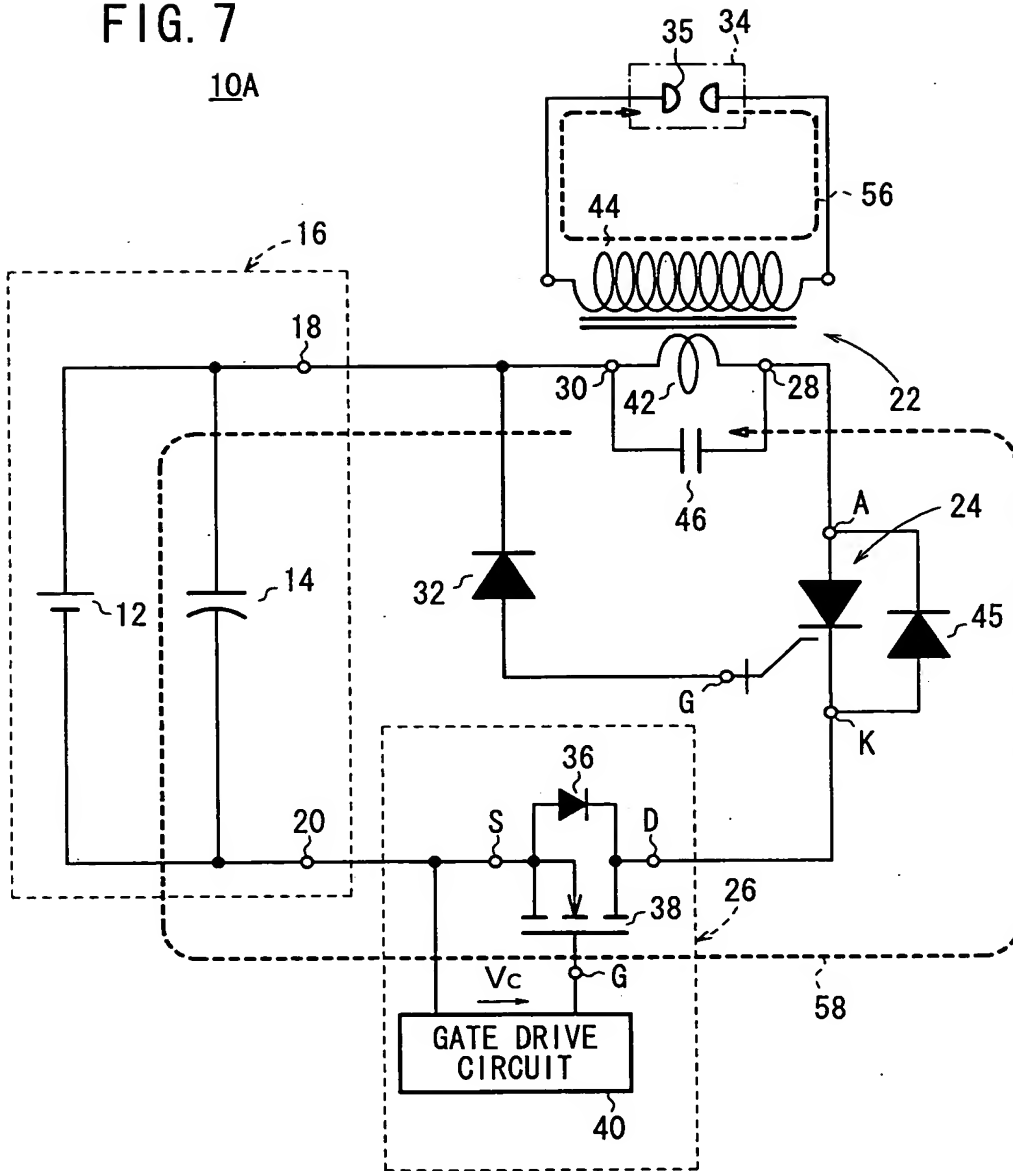


FIG. 8

10Aa

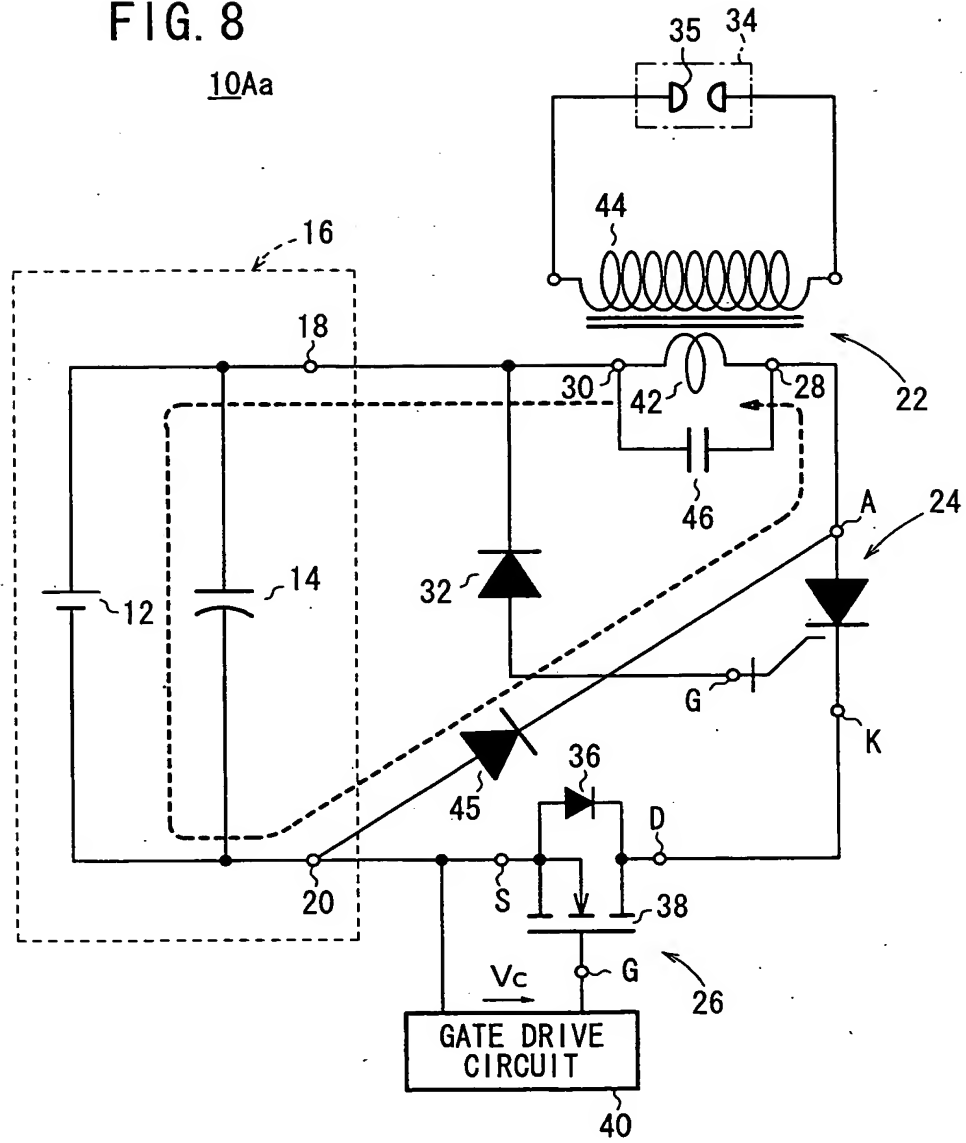


FIG. 9

10B

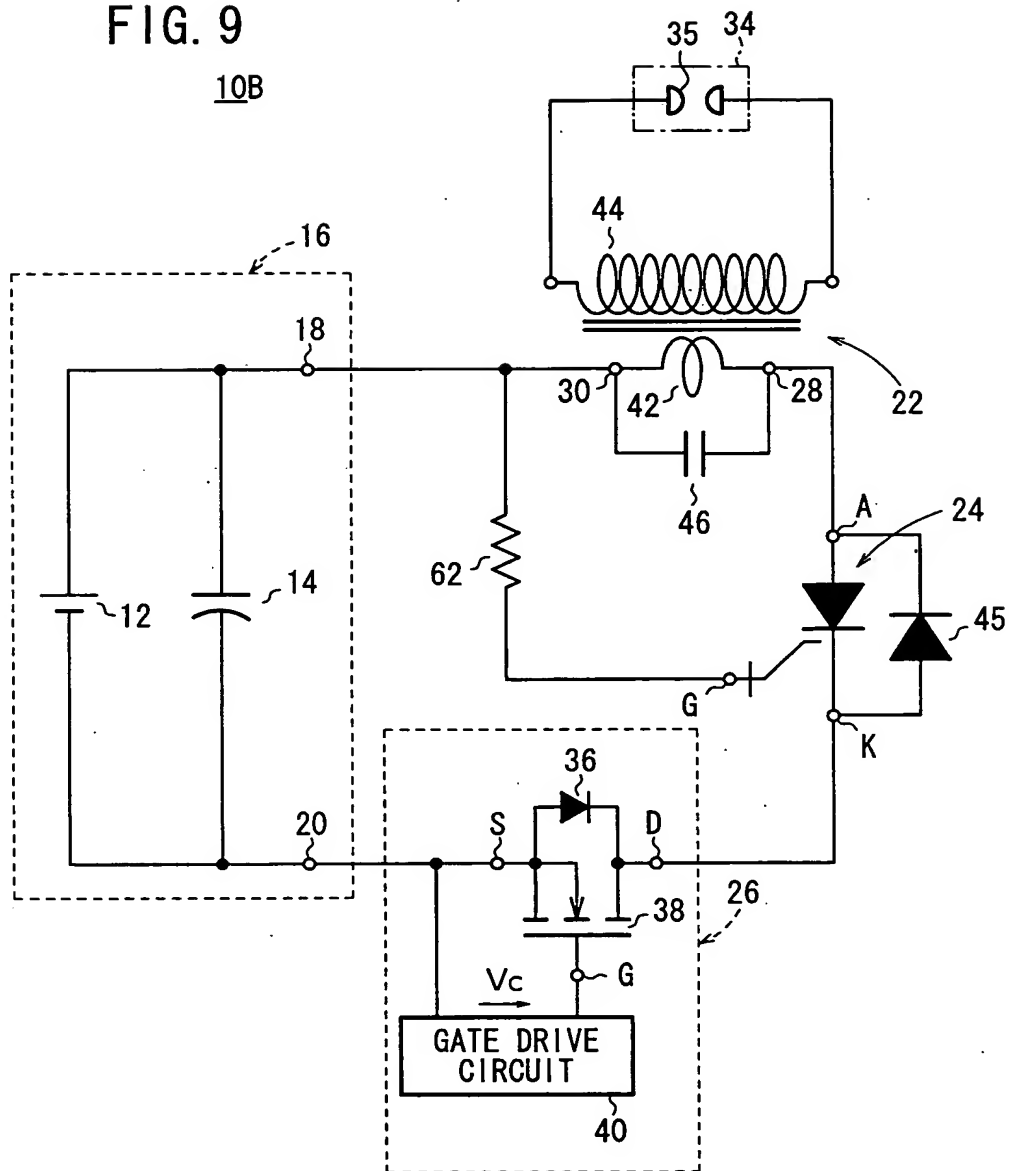


FIG. 10

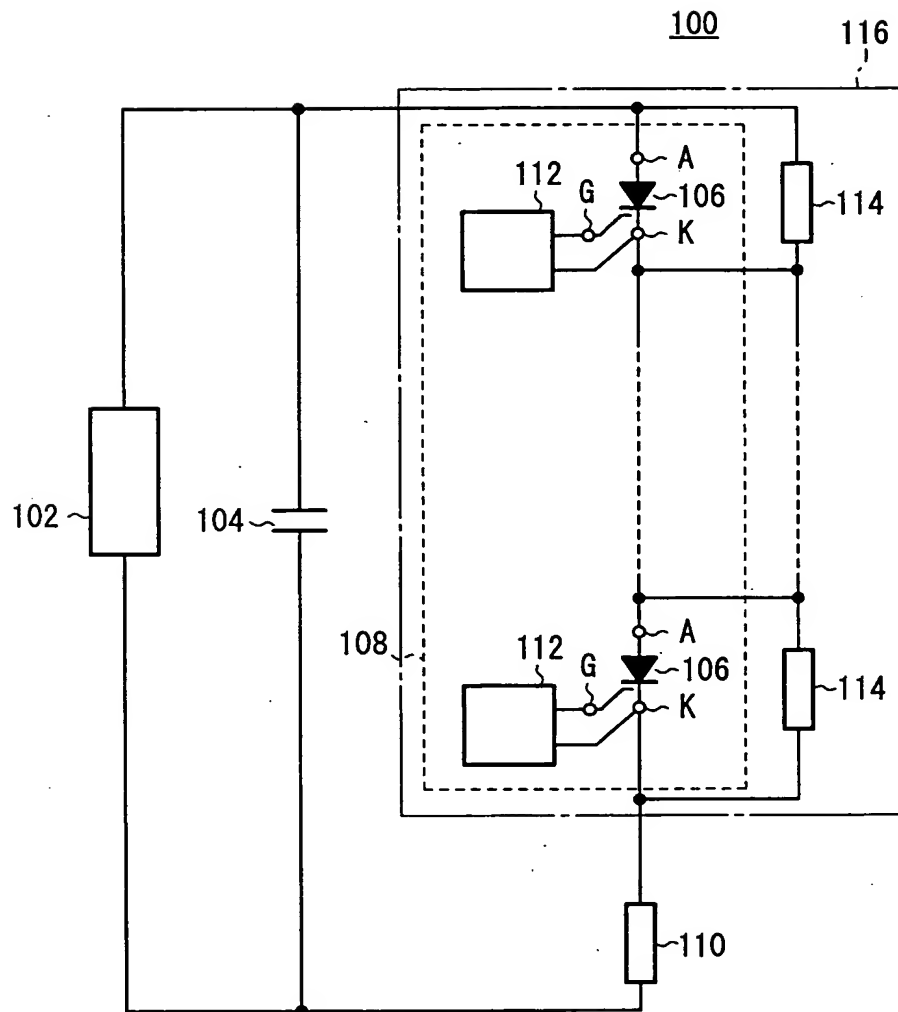


FIG. 11

